



Docket No.: P2002,0599

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Till Schlösser, et al.  
Applic. No. : 10/619,970  
Filed : July 15, 2003  
Title : Integrated Semiconductor Memory and Fabrication Method

**ASSOCIATE POWER OF ATTORNEY**

Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

Please recognize MARKUS NOLFF (Reg. No. 37,006) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

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Respectfully submitted,

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Date: August 20, 2003

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